Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet	2	**of**	2	

Complete if Known	
Application Number	10/658,897
Filing Date	September 10, 2003
First Named Inventor	Vijay S. Menon
Art Unit	2189
Examiner Name	Flournoy, Horace L.
Attorney Docket Number	42P17027

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
✗		W. Pugh, The Java Memory Model is Fatally Flawed. Concurrency: Practice and Experience, vol. 12, no. 6, pp. 445-455, 2000.	
✗		M. Ahamad, et al. The Power of Processor Consistency. In Proceedings of the Fifth ACM Symposium on Parallel Algorithms and Architectures (SPAA), 1993.	
✗		D. Scales. Impact of "reads kill" in Java. http://www.cs.umd.edu/~pugh/java/readsKillImpact.html, May 1999.	
✗		Gontmakher and A. Schuster. Java Consistency: Non-operational Characterizations for the Java Memory Behavior. Technical Report CS0922, Dept of C.S., Technion, Nov. 1997.	
✗		Intel Itanium Archticture Software Developer's Manual, Vol. 1: Application Architecture, Revision 2.o, pp. 1:58-1:62, December, 2001.	
✗		R.R. Goodman. Cache Consistency and Sequential Consistency. Technical Report 61, IEEE Scalable Coherent Interface Working Group, March 1989.	

Examiner Signature		Date Considered	5/30/2006